INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/592,106	
Filing Date	06/12/2000	_
First Named Inventor	Stephen Melvin	
Art Unit	2154	
Examiner Name	Larry D. Donaghue	
Attorney Docket Number	P3813	

			U. S. PATEN	F DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
5		^{US-} 5600837 A *	02-04-1997	Artieri, Alain	
B.		US- 5461722 A *	10-24-1995	Goto, Tatuhiro	
2		^{US-} 5542088 A *	07-30-1996	Jennings, Jr. et al.	
5		US- 5748468 A *	05-05-1998	Notenboom et al.	2505MED
1/2		US- 5745778 A *	04-28-1998	Alfieri, Robert	RECEIVED
02		^{US-} 5867725 A *	02-02-1999	Fung et al.	
		^{US-} 5309173 A *	05-03-1994	Izzi et al.	APR 1 6 2004
Wo		US- 5852726 A *	12-22-1998	Lin et al.	E Landau Cantor 010
Mh		US- 5713038 A *	01-27-1998	Motomura, Masato	Fechnology Center 210
A)		US- 5812811 A *	09-22-1998	Dubey et al.	
Θ_{λ}		^{US-} 5860017 A *	01-12-1999	Sharangpani et al.	
Th		^{US-} 5784613 A *	07-21-1998	Tamirsa	
N/L		^{US-} 5511210 A *	04-23-1996	Nishikawa et al.	
Dr		US- 6119203 A *	09-12-2000	Snyder et al.	
		US- 5649144 A *	07-15-1997	Gostin et al.	
		US- 6061710 A *	05-09-2000	Eickemeyer et al.	
Sel		^{US-} 6076157 A *	06-13-2000	Borkenhagen et al.	
1/1	7	^{US-} 6029228 A *	02-22-2000	Cai et al.	
		^{US-} 5561776 B1 *	10-01-1996	Popescu et al.	

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code* Number* "Kind Code* (if known)	GN PATENT DOCU Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	٦٠
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Application Number 09/592,106

Filing Date 06/12/2000

First Named Inventor Stephen Melvin

Art Unit 2154

Examiner Name Larry D. Donaghue

Attorney Docket Number P3813

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V)		^{US-} 6535905 B1 *	03-18-2003	Kalafatis et al.	
4		US- 4200927 A *	04-29-1998	Hughes et al.	
0		US- 5933627 A *	08-03-1999	Parady, Bodo	
10)		^{US-} 5758142 A *	05-26-1998	McFarling et al.	
\$		^{US-} 6016542 A *	01-18-2000	Gottlieb et al.	
		US- 5361337 A *	11-01-1994	Okin, Kenneth	RECEIVE
\circ		us- 6308261 B1 *	10-23-2001	Morris et al.	
2		us- 5946711 A *	08-31-1999	Donnelly, Stephen	APR 1.6 2004
\mathcal{X}		^{US-} 5815733 A *	09-29-1998	Anderson et al.	- i Onnier
X		^{US-} 5987492 A *	11-16-1999	Yue, Kelvin	Technology Center
2		US- 6018759 A	01-25-2000	Doing et al.	
2		^{US-} 6052708 A *	04-18-2000	Flynn et al.	
Ć		^{US-} 6212544 B1 *	04-03-2001	Borkenhagen et al.	
		US- 5546593 A *	08-13-1996	Kimura et al.	
0		us- 5913049 A *	06-15-1999	Shiell et al.	
		US-			
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First Named Inventor	Stephen Melvin	APR 1 6 2004
Art Unit	2154	APR 1.0 2004
Examiner Name	Larry D. Donaghi	reTechnology Center 2
Attorney Docket Number	P3813	

		NON PATENT LITERATURE DOCUMENTS	
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7		* FISKE et al., "Thread Prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors", 1995, pp 210 -221, IEEE	
A	:	* YAMAMOTO, Wayne, "An Analysis of Multistreamed, Superscalar Processor Architectures", University of California Santa Barbara dissertation, December 1995, Santa Barbara, US	
1		* STEERE et al., "A Feedback - Driven Proportion Allocator for Real-Rate Scheduling", Third Symposium on Operating Systems Design and Implementation, February 1999, pp 145-158, USENIX Association	-
W		* YAMAMOTO et al., "Increasing Superscalar Performance Through Multistreaming", 1995	
D		 TULLSEN et al., "Simultaneous Multithreading: Maximizing on-Chip Parallelism", 22nd Annual International Symposium on Computer Architecture, June 1995, Santa Margherita Ligure, Italy 	
1 5.		* YOAZ et al., "Speculation Techniques for Improving Load Related Instruction Scheduling", 1999, pp 42-53, IEEE	
1		* KESSLER, R.E., "The Alpha 21264 Microprocessor: Out-of-Order Execution at 600 Mhz", August 1998	
10		* YAMAMOTO ET AL., Performance Estimation of Multistreamed, Superscalar Processors, IEEE. 1994, pp 195-204, Hawaii, US	
0		* NEMIROVSKY et al., "Quantitative Study of Data Caches on a Multistreamed Architecture", Workshop on Multithreaded Execution Architecture and Compilation, January 1998	
		 LI et al., "Design and Implementation of a Multiple-Instruction-Stream Multiple-Execution-Pipeline Architecture". 7th International Conference on Parallel and Distributed Computing and Systems, October 1995, Washington D.C. 	

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		NON PATENT LITERATURE DOCUMENTS	
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5		* The PowerPC Architecture: A Specification for a New Family of RISC Processors, Second Edition, May 1994, pp 70-72, Morgan Kaufmann, San Francisco	
	_	* MC68020 32-Bit Microprocessor User's Manual, Third Edition, 1989, pp 3-125, 3-126, and 3-127, Prentice Hall, New Jersey	
9	,	 POTEL, M.J., Real-Time Playback in Animation Systems, Proceedings of the 4th Annual Conference on Computer Graphics and Interactive Techniques, 1977, pp. 72-77, San Jose, CA 	
Q)		* ARM Architecture Reference Manual, 1996, pp. 3-41, 3-42, 3-43, 3-67, 3-68, Prentice Hall	
D		* ESA/390 Principles of Operation, IBM Library Server, 1993 , Table of Contents and Para. 7.5.31 and 7.5.70 (available at : http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS)	
B		* MC88110 Second Generation RISC Microprocessor User's Manual, 1991, pp. 10-66, 10-67 and 10-71, Motorola, Inc.	
\$		* DIEFENDORFF, KEITH et al., Organization of the Motorola 88110 Superscalar RISC Microprocessor, IEEE Micro, April 1992, pp.40-63, Vol. 12, No. 2	
		* KANE, GERRY, PA-RISC 2.0 Architecture, 1996, pp 7-106 and 7-107, Prentice Hall, New Jersey	
Ø		* DIEFENDORFF, KEITH et al., AltiVec Extension to PowerPC Accelerates Media Processing, March - April 2000, pp. 85-95, IEEE Micro, Vol. 20, No. 2	
0	¥	* NEMIROVSKY ET AL., DISC: Dynamic Instruction Stream Computer, ACM, 1991, pp 163-171	

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I))	* DONALSON ET AL., "DISC: Dynamic Instruction Stream Computer, An Evaluation of Performance", 26th Hawaii Conference on Systems Sciences, 1993, pp 448-456, vol. 1		
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First Named Inventor Stephen Melvin
Mario D. Nemirovsky

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brotude name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the litem (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), votume-Issue number(s), publisher, city and/or country where published. GRUNEWALD, WINFRIED et al., Towards Extremely Fast Context Switching in a Block-Multthreaded Processor, Proceedings of EUROMICRO-22, 1996, pp.592-599. BRADFORD, JEFFREY et al., Efficient Synchronization for Multithreaded Processors, Workshop on Multithreaded Execution, Architecture and Compilation, January-February 1998, pp.1-4 TULLSEN, DEAN et al., Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor, UCSD CSE Technical Report CS98-587, June 1998, all pages, US.
Processor, Proceedings of EUROMICRO-22, 1996, pp.592-599. BRADFORD, JEFFREY et al., Efficient Synchronization for Multithreaded Processors, Workshop on Multithreaded Execution, Architecture and Compilation, January-February 1998, pp.1-4 TULLSEN, DEAN et al., Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor, UCSD CSE Technical Report CS98-587, June 1998, all pages, US.
Multithreaded Execution, Architecture and Compilation, January-February 1998, pp.1-4 TULLSEN, DEAN et al., Supporting Fine-Grained Synchronization on a Simultaneous Multithreading Processor, UCSD CSE Technical Report CS98-587, June 1998, all pages, US.
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First Named Inventor Stephen Melvin Mario D. Nemirovsky

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Examiner Name Larry Donaghue

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9	AA	DIEFENDORF pages (Decem	F, KEI ber 7,	TH, "WinChip 4 Thumbs 1998)	s Nose at ILP," Microproces	sor Report, Vol. 12, No.16, 10		
9	AB	EGGERS ET . 1997.	EGGERS ET AL, "Simultaneous Multithreading: A Platform for Next-Generation Processors", IEEE Micro, 1997.					
0	AC	DIEFENDORF 1998.	F, KEI	TH; "Jalapeno Powers C	yrix's M3", Microprocessor R	Report, Vo. 12, No. 15, Nov. 16,		
4/	AD	BECKER ET A	AL; "The	PowerPC 601 Microproc	cessor, IEEE Micro, IEEE 19	93.		
\mathscr{Q}	ΑE	SLATER, MIC 1998.	HAEL,	"Rise Joins x86 Fray W	ith mP6" Microprocessor Re	port, Vol. 12, No. 15, Nov. 16,		
Q	AF	DIEFENDORF	DIEFENDORFF, KEITH; "Compaq Chooses SMT for Alpha", Microprocessor Report, Dec. 6, 1999.					
D	AG	POTEL, M.J., Computer Gra	POTEL, M.J., "Real-time Playback in Animation Systems", Proceedings of the 4 th Annual Conference on Computer Graphics and Interactive Techniques, 1977, pp.72-77, San Jose, CA.					
	AH	ARM Architect	ARM Architecture Reference Manual, 1996, pp.3-41, 3-42, 3-43, 3-67, 3-68, Prentice Hall.					
Q	AI	MC88110 Sec Motorola, Inc.	cond Ge	eneration RISC Micropro	cessor User's Manual, 1991	1, pp.10-66, 10-67, and 10-71,		
	AJ	DIFENDORFF IEEEE Micro, A	, KEITI April 19	H et al., Organization of 92, pp.40-63, Vol. 12, No.	of the Motorola 88110 Supe 0.2.	erscalar RISC Microprocessor,		
A)	AK	KANE, GERRY	Y, PA-R	ISC 2.0 Architecture, 19	96, pp.7-106 and 7-107, Pren	tice hall, New Jersey.		
9	AL	DIEFENDORFF, KEITH et al., "AltiVec Extension to PowerPC Accelerates Media Processing", March – April 2000, pp.85-95, IEEE Micro, Vol. 20, No.2.						
821	AM	FISKE et al., Processors", 1	FISKE et al., "Thread Prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors", 1995, pp.210-211, IEEE.					
	AN	YAMAMOTO, CA, Santa Bar	YAMAMOTO, Wayne, "An Analysis of Multistreamed, Superscalar Processor Architectures", University of CA, Santa Barbara dissertation, December 1995, Santa Barbara, CA.					
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	Mario D. Nemirovsky				
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9	AP	YAMAMOTO, WAYNE, et al., "Increasing Superscalar Performance Through Multistreaming", 1995.					
9	AQ	TULLSEN, DEAN, et al., "Simultaneous Multithreading: Maximizing on-Chip Parallelism", 22 nd Annual International Symposium on Computer Architecture, June 1995, Santa Margherita, Ligure, Italy.					
1	AR	YOAZ et al., "Speculation Techniques for Improving Load Related Instruction Scheduling", 1999, pp.42-53. IEEE.	:				
D	AS	KESSLER, R.E., "The Alpha 21264 Microprocessor: Out-of -Order Execution at 600 Mhz", August 1998.					
9	AT	"ESA/390 Principles of Operation" Table of Contents. 09/23/03; http://publibz.boulder.ibm.com/cgl-bin/bookmgr_OS390/BOOKS/DZ9AR001/CCONTENTS.					
4/	ΑU	NEMIROVSKY, MARIO D. et al; "DISC: DYNAMIC INSTRUCTION STREAM COMPUTER", 1191; pp.163-171.					
	AV	NEMIROVSKY, MARIO D., "DISC, A DYNAMIC INSTRUCTION STREAM COMPUTER", September 1990.					
8	AW	YAMAMOTO, WAYNE et al; "Performance Estimation of Multistreamed, Superscaler Processors", 1994 IEEE, pp. 195-204.					

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